

**REMARKS**

In the Office Action of August 25, 2004, it was noted that the application contains claims directed to the following patentably distinct species of the claimed invention:

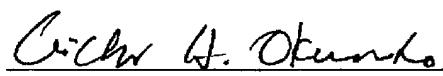
Species I claims 1-25 (corresponding to Figures 5-7), drawn to a clock processing logic having edge detection logic and sensitivity adjustment logic.

Species II claims 26-46 (corresponding to Figures 8-16), drawn to a clock processing logic having an edge filter, sample accumulation logic, and clock period jitter processing logic.

In response to the election requirement, applicants hereby elect Species I claims 1-25 for prosecution on the merits in this patent application. Accordingly, Species II claims 26-46 have been withdrawn from consideration in this patent application.

Respectfully submitted,

Dated: August 31, 2004

  
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Applic. Serial No. 10/621,055

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